

REMARKS

At the outset, the Applicants thank the Examiner for the thorough review and consideration of the subject application. The Non-Final Office Action of February 27, 2002, has been received and its contents carefully noted. Claims 1 and 6 have been amended to better conform to U.S. practice and claims 11-18 are newly added. Accordingly, claims 1-18 are currently pending.

Claims 1-10 were rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 6,229,513 issued to Nakano, *et al.* ("Nakano"). Applicants respectfully traverse these rejections and reconsideration is hereby requested.

35 U.S.C. § 102

The Examiner rejected claims 1-10 under 35 U.S.C. §102(e) as allegedly being unpatentable over Nakano. Applicants respectfully traverse this rejection and reconsideration is hereby requested.

Claim 1 is allowable as recites a combination of elements including, for example, "a system including an image processing part for deciding a timing format of an image data and generating a control signal for the image data; an encoder for encoding the image data and the control signal output from the image processing part into a RSDS specification." None of the cited references singly or in combination teaches or suggests at least these features of the claimed invention.

The Examiner states, "the display control unit 110 outputs an output polarity control signal to drain drivers 130 through signal line 135 (column 6, lines 46-49) corresponding to the

control signal output from the image processing part in an RSDS specification ...and an interface unit Fig. 1 (100) corresponding to the control board” (Office Action at 2 to 3.)

However, claim 1 requires the encoder being part of the system, also Nakano fails to teach or suggest, “an encoder for encoding the image data and the control signal output from the image processing part in to a RSDS specification” as required by claim 1.

In contradistinction, Nakano discloses:

The display control unit 110 also outputs a clock signal D1 for controlling output timing (hereinafter referred to as "clock signal D1") to the drain drivers 130 through a signal line 133 when display data are completed for one horizontal scan period. The display control unit 110 outputs an output polarity control signal (hereinafter referred to as "an alternating signal") to the drain drivers 130 through a signal line 135.

U.S. Patent No. 6,229,513 (issued May 8, 2001)(column 6, lines 41-50).

Accordingly, Applicants respectfully submit that independent claim 1, and claims 3-5, that depend from independent claim 1 are allowable and request the rejection under 35 U.S.C. § 102(e) be withdrawn as a prima facie case of anticipation has not been established.

Claim 6 is allowable as recites a combination of elements including, for example, “a signal converting board including ... an encoder for encoding the digital data and the digital control signal output from the image processing part into a RSDS specification.” None of the cited references singly or in combination teaches or suggests at least these features of the claimed invention for the similar reasons as discussed above. Accordingly, Applicants respectfully submit that independent claim 6 and claims 7-10, that depend from claim 6, are allowable and request the rejection under 35 U.S.C. § 102(e) be withdrawn as a prima facie case of anticipation has not been established.

Other Matters

Newly added claims 11-18 are allowable, as independent claim 11 recites a combination of elements including, for example, “a system including a image signal processing part, a power output part, and encoder part, wherein the image signal processing part for generating a data signal and a control signal the encoder receiving the data signal and the control signal and converting into a RSDS signals.” None of the cited references singly or in combination teaches or suggests at least these features of the claimed invention. Accordingly, Applicants respectfully submit that independent claim 11 and claims 12-18, that depend from claim 11 are allowable.


Conclusion

If the Examiner believes, for any reason, that personal communication will expedite prosecution of this application, the Examiner is invited to telephone the undersigned at the number provided.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. § 1.136, and any additional fees required under 37 C.F.R. § 1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 23-1951. Please credit any overpayment to deposit Account No. 23-1951.

Prompt and favorable consideration of this Amendment is respectfully requested.

Respectfully submitted,



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